

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number		10001477
Filing Date		2001-11-01
First Named Inventor	Steve Roe	
Art Unit	2123	
Examiner Name	Jason Scott Proctor	
Attorney Docket Number	CD01203M	

19	USPTO Non-Final Rejection for Application Number 10/327,207 (CD01192) dated 09/20/2005; 11 pages	<input type="checkbox"/>
20	USPTO Miscellaneous Action for Application Number 10/327,207 (CD01192) dated 05/13/2003; 1 page	<input type="checkbox"/>
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22	USPTO Non-Final Rejection for Application Number 09/989,570 (CD01167M) dated 01/26/2005; 9 pages	<input type="checkbox"/>
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30	USPTO Non-Final Rejection for Application Number 09/989,571 (CD01168M) dated 05/23/2005; 6 pages	<input type="checkbox"/>
31	USPTO Final Rejection for Application Number 09/989,571 (CD01168M) dated 01/26/2005; 11 pages	<input type="checkbox"/>
32	USPTO Non-Final Rejection for Application Number 09/989,571 (CD01168M) dated 07/12/2004; 9 pages	<input type="checkbox"/>
33	M. Morris Mano, "Computer System Architecture," 1982, Prentice-Hall, 2nd Edition, pp. 261-264 and 435-440; 14 pages	<input type="checkbox"/>
34	Dirk Killat, "A One-Chip Solution for Electronic Ballasts in Fluorescent Lamps," Power Electronics, < http://powerelectronics.com/mag/power_onechip_solution_electronic/ >, dated 03/01/2004, accessed 09/13/2005; 4 pages	<input type="checkbox"/>
35	Fred Eady, "PSOC 101," Circuit Cellar, August 2004, accessed 09/13/2005, < http://www.circuitcellar.com/library/print/0804/eady169/2.htm >; 4 pages	<input type="checkbox"/>
36	USPTO Notice of Allowance for Application Number 10/803,030 (CD00167C1) dated 01/08/2007; 6 pages	<input type="checkbox"/>
37	USPTO Non-Final Rejection for Application Number 10/803,030 (CD00167C1) dated 06/08/2005; 4 pages	<input type="checkbox"/>
38	Application Number 12/058,586 (CD2007122): "System and Method for Monitoring a Target Device," Ogami et al., filed on 03/28/2006; 41 pages	<input type="checkbox"/>
39	Application Number 12/058,534 (CD2007123): "System and Method for Controlling a Target Device," Ogami et al., filed on 03/28/2006; 40 pages	<input type="checkbox"/>
40	Application Number 12/004,833 (CD2007124): "Systems and Methods for Dynamically Reconfiguring a Programmable System on a Chip," Ogami et al., filed on 12/21/2007; 40 pages	<input type="checkbox"/>

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41	Application Number 12/058,569 (CD2007125): "Configuration of Programmable IC Design Elements," Best et al., filed on 03/28/2008; 19 pages	<input type="checkbox"/>
42	Application Number 12/057,149 (CD2007156): "Power Management Architecture, Method and Configuration System," Kenneth Ogami, filed on 03/27/2008; 34 pages	<input type="checkbox"/>
43	Application Number 12/765,400 (CD2009119): "Autonomous Control in a Programmable System," Sullam et al., filed on 04/22/2010; 30 pages	<input type="checkbox"/>
44	International Search Report for International Application Number PCT/US10/33626 (CD2009119PCT) mailed 06/24/2010; 3 pages	<input type="checkbox"/>
45	The Written Opinion of the International Search Report for International Application Number PCT/US10/33626 (CD2009119PCT) mailed 06/24/2010; 5 pages	<input type="checkbox"/>
46	USPTO Non-Final Rejection for Application Number 12/060,176 (CD2007136) dated 03/30/2010; 22 pages	<input type="checkbox"/>
47	John Mangino, "Using DMA with High Performance Peripherals to Maximize System Performance," 2007, Texas Instruments, pp. 1-23; 23 pages	<input type="checkbox"/>
48	Balough et al., "White Paper: Comparing IP Integration Approaches for FPGA Implementation," February 2007, Version 1.1, Altera, pp. 1-7; 7 pages	<input type="checkbox"/>
49	A.F. Harvey, "DMA Fundamentals on Various PC Platforms," 2001, 2004, National Instruments Corporation, pp. 1-19; 19 pages	<input type="checkbox"/>
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U.S. PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	5935233	B1	1999-08-10	Jeddeloh, Joseph	Entire Document
	2	4807183	B1	1989-02-21	Kung et al.	Entire Document
	3	5377333	B1	1994-12-27	Nakagoshi et al.	Entire Document
	4	5390173	B1	1995-02-14	Spinney et al.	Entire Document
	5	5866480	B1	1997-09-09	Leung et al.	Entire Document
	6	5956279	B1	1999-09-21	Mo et al.	Entire Document
	7	5530673	B1	1996-06-25	Tobita et al.	Entire Document
	8	5500823	B1	1996-03-19	Martin et al.	Entire Document

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	9	7516509	B1	2009-11-10	Qureshi et al.	Entire Document
	10	7193901	B1	2007-03-20	Ruby et al.	Entire Document
	11	6754101	B1	2004-06-22	Terzioglu et al	Entire Document
	12	6853598	B1	2005-02-08	Chevallier, Christopher J.	Entire Document
	13	6215352	B1	2001-04-10	Sudo, Naoki	Entire Document
	14	5708589	B1	1998-01-13	Beauvais, David L.	Entire Document
	15	6014723	B1	2000-01-11	Tremblay et al.	Entire Document
	16	3805245	B1	1974-04-16	Brooks et al.	Entire Document
	17	6496971	B1	2002-12-17	Lesea et al.	Entire Document
	18	5099191	B1	1992-03-24	Geller et al.	Entire Document
	19	5184061	B1	1993-02-02	Lee et al.	Entire Document

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20	5497119	B1	1998-03-05	Tedrow et al.	Entire Document
21	5844404	B1	1998-12-01	Caser et al.	Entire Document
22	6552933	B1	2003-04-22	Roohparvar, Frankie F.	Entire Document
23	7034803	B1	2006-04-25	Brady et al.	Entire Document

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	1	20070258458	A1	2007-11-08	Kapoor, Rohit	Entire Document
	2	20050066152	A1	2005-03-24	Garey, Kenneth	Entire Document
	3	20090322305	A1	2009-12-31	De Cremoux, Guillaume	Entire Document

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FOREIGN PATENT DOCUMENTS

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	1	USPTO Notice of Allowance for Application Number 09/943,062 (CD00228M) dated 06/29/2010; 4 pages	<input type="checkbox"/>
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9	USPTO Advisory Action for Application Number 11/818,005 (CD01171C) dated 07/30/2010; 3 pages	<input type="checkbox"/>
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